

PTO-1449		Application No. 10/824,873		Applicant(s) Shannon V. Davidson			
Information Disclosure Citation In an Application		Docket Number 064747.1016		Group Art Unit 2173	Filing Date 15 April 2004		
<b>U.S. PATENT DOCUMENTS</b>							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
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<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
NY.C./	F	WO 03/005292 A1	01/16/2003	PCT			
<b>NON-PATENT DOCUMENTS</b>							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
NY.C./	F	The Patent Office of the State Intellectual Property Office of the People's Republic of China, Office Action for Application No. 200510081719.3, 11 pages, Date of Dispatch 04/20/2007					04/20/2007
NY.C./	G	Hovestadt et al., "Scheduling in HPC Resource Management Systems: Queuing vs. Planning, Proceedings of the 9th Workshop on Job Scheduling Strategies for Parallel Processing, Seattle, WA, pages 1-19, June 2003					June 2003
NY.C./	H	Keller et al., "Anatomy of a Resource Management System for HPC Clusters," Annual Review of Scalable Computing, vol. 3, pp. 1-23, 2001					2001
	I						
	J						
	K						
EXAMINER /Yi Chen/				DATE CONSIDERED 11/26/2007			
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<b>U.S. PATENT DOCUMENTS</b>							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
Y.C./	A	6,167,502	12/26/2000	Pechanek et al.			
Y.C./	B	2001/0049740	12/01/2001	Karpoff			
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<b>FOREIGN PATENT DOCUMENTS</b>							
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<b>NON-PATENT DOCUMENTS</b>							
		<b>DOCUMENT (Including Author, Title, Source, and Pertinent Pages)</b>					<b>DATE</b>
/M.C./	<b>E</b>	Hans-Ulrich Heiss, "Processor Management in Two-Dimensional Grid-Architectures," Interner Bericht Nr. 20/92, XP002416087, 51 pages					Dec. 1992
/M.C./	<b>F</b>	EPO Registered Letter, Application No. 05 252 239.8-1243, 6 pages					02.02.2007
	<b>G</b>						
	<b>H</b>						
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	<b>J</b>						
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N.C./	A	The Intellectual Property Bureau Ministry of Economic Affairs, Office Action for Application No. 94111492, received November 27, 2006, 2 pages					Received 11/27/2006
N.C./	B	Jiun-Sheng et al., "Network Topology Structure on Board of Computer Cluster," Publication No. TW532011B, Data Supplied from the esp@cenet database - Worldwide, 1 page					Received 12/18/2006
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Y.C./	A	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012500 and Written Opinion of the International Search Authority, mailed 08/01/2005, 12 pages				08/01/2005
Y.C./	B	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012489 and Written Opinion of the International Search Authority, mailed 11/18/2005, 14 pages				11/18/2005
Y.C./	C	Unknown, "HP AlphaServer SC User Guide," Internet Article, Online!, July 12, 2004, retrieved from the internet: URL: <a href="http://web1.quadrics.com/onlinedocs/AlphaServer/Eagle/html/AlphaServerUserGuide/">http://web1.quadrics.com/onlinedocs/AlphaServer/Eagle/html/AlphaServerUserGuide/</a> retrieved on 07/19/2005				07/12/2004
Y.C./	D	Choo et al., "An Efficient Submesh Allocation Scheme for 3D Torus Multicolor Systems," Parallel Algorithms/Architecture Synthesis, 1997, Proceedings, Second Aizu International Symposium Aizu-Wakamatsu, Japan, March 17-21, 1997				03/17/1997 - 03/21/1997
Y.C./	E	Unknown, "Message Passing Interface (MPI)," Internet Article, Online!, December 23, 2003, pages 1-33, retrieved from the internet: URL: <a href="http://web.archive.org/web/20040102194825/http://www.llnl.gov/computing/tutorial/smpi/">http://web.archive.org/web/20040102194825/http://www.llnl.gov/computing/tutorial/smpi/</a> , retrieved on 07/18/2005, pages				12/23/2003
Y.C./	F	Panagiotis, Christias et al., "inetd - Internet Services Daemon," pages 1-4, 1994 Man-cgi 1.15S, 1995 Modified for Solaris 2.3				1994
Y.C./	G	Unknown, "Cisco Local Director Configuration and Command Reference Guide," Internet Article, Online, April 4, 2001				04/04/2001
Y.C./	H	Moore et al., "Managing Mixed-Use Clusters with Clusters-onDemand," Internet Article, November 2002				November 2002
Y.C./	I	Di Santo et al., "Kernel Implementation of Locality-Aware Dispatching Techniques for Web Server Clusters," Cluster Computing, 2003 Proceedings, Pages 154-162				12/01/2003
Y.C./	J	Ross et al., "3.3 Connectionless Transport," February 22, 2001, pages 1-4, retrieved on 11/01/2005				11/02/2005
Y.C./	K	Davidson et al., Patent Application Serial No. 10/825,345, entitled, "System and Method for Computer Cluster Virtualization Using Dynamic Boot Images and Virtual Disk, 47 pages of specification, 10 pages of drawings, Attorney Docket No. 064747.1011				04/15/2004
Y.C./	L	Davidson, Patent Application Serial No. 10/991,598, entitled, "Scheduling in a High Performance Computing (HPC) System", 111 pages specification, claims and abstract, 11 pages of drawings, Attorney Docket No. 064747.1048				11/17/2004
Y.C./	M	Davidson, Patent Application Serial No. 10/991,994, entitled, "On-Demand Instantiation in a High-Performance Computing (HPC) System", 110 pages specification, claims and abstract, 12 pages of drawings, Attorney Docket No. 064747.1049				11/17/2004
EXAMINER			DATE CONSIDERED			
/Yi Chen/			11/26/2007			
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## U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
/Y.C./	A	5,280,607	01-18-1994	Bruck et al.			
/Y.C./	B	5,805,785	09-08-1998	Dias et al.			
/Y.C./	C	6,088,330	07-11-2000	Bruck et al.			
/Y.C./	D	6,460,149	10-01-2002	Rowlands et al.			
/Y.C./	E	6,477,663	11-05-2002	Laranjeira et al.			
/Y.C./	F	6,496,941	12-17-2002	Segal et al.			
/Y.C./	G	6,718,486	04-06-2004	Rosellin et al.			
/Y.C./	H	6,820,221	11-16-2004	Fleming			
/Y.C./	I	6,918,051	07-12-2005	Block et al.			
/Y.C./	J	6,918,063	07-12-2005	Ho et al.			
/Y.C./	K	6,952,766	10-04-2005	Dervin et al.			
/Y.C./	L	7,016,299	04-21-2006	Kashyan			
/Y.C./	M	7,028,228	04-11-2006	Lovy et al.			
/Y.C./	N	7,093,004	08-15-2006	Bernardin et al.			
/Y.C./	O	2003/0009551	01-09-2003	Benfield et al.			

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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## NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
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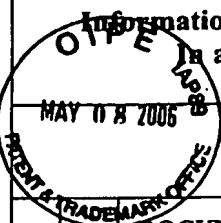
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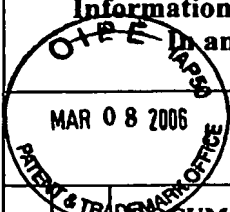
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Y.C./	A	5,682,491	10/28/1997	Pechanek et al.			
Y.C./	B	6,735,660	05/11/2004	Osten et al.			
Y.C./	C	2003/0188071	10/02/2003	Kunjan et al.			
Y.C./	D	2004/0024949	02/05/2004	Winkler et al.			
Y.C./	E	2004/0268000	12/30/2004	Barker et al.			
<b>FOREIGN PATENT DOCUMENTS</b>							
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<b>NON-PATENT DOCUMENTS</b>							
		<b>DOCUMENT (Including Author, Title, Source, and Pertinent Pages)</b>					<b>DATE</b>
Y.C./	H	Wong, William, "Switch-Chip Fuels Third-Generation InfiniBand," November 10, 2003, Electronic Design, 2 pages					11/10/2003
	I						
	J						
<b>EXAMINER</b>				<b>DATE CONSIDERED</b>			
/Yi Chen/				11/26/2007			
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U.S. PATENT AND TRADEMARK OFFICE

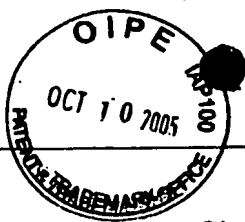
PTO-1449		Application No. 10/824,873		Applicant(s) Shannon V. Davidson			
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		U.S. PATENT DOCUMENTS					
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A	2003/0154112	08/14/2003	Neiman et al.	705	5	10/29/2002
	B						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
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NON-PATENT DOCUMENTS							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012643, 6 pages, 04/19/2006, and Written Opinion of the International Search Authority, 5 pages					Mailed 04/19/2006
	F	Haynes et al., "A Visualization Tool for Analyzing Cluster Performance Data," 42nd Annual Symposium on Foundations of Computer Science, (FOCS 2001), October 8, 2001, pages 295-302					October 14-17 2001
	G	Patel et al., "Sage: An Application Development Tool Suite for High Performance Computing Systems," Aerospace Conference Proceedings, 2000, IEEE, Vol 11, pages 491-500					March 18-25 2000
	H	Baraglia et al, "RsdEditor: A Graphical User Interface for Specifying Metacomputer Components," Heterogeneous Computing Workshop Proceedings, 9th Cancun, Mexico, pages 336-345					May 1, 2000
	I						
EXAMINER /Yi Chen/				DATE CONSIDERED 11/26/2007			
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<b>U.S. PATENT DOCUMENTS</b>							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
N.C./	A	5,301,104	04/05/1994	Yalamanchili	395	800	08/07/1990
	B						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
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	D						
<b>NON-PATENT DOCUMENTS</b>							
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)					DATE
N.C./	E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012314, 6 pages, 09/20/2005, and Written Opinion of the International Search Authority, 7 pages					Mailed 02/10/2006
N.C./	F	Feitelson, "Job Scheduling in Multiprogrammed Parallel Systems", IBM Research Report, August 1997, pages 1-4, XP002942107					August 1997
N.C./	G	Kandlur et al., "Hypercube Management in the Presence of Node Failures", third conference on hypercube concurrent computers and applications ACM New York, 1988, pages 328-336, vol. 1, XP002364194					1998
N.C./	H	Wenjian et al., "Efficient Processor Allocation for 3D Tori", Parallel Processing Symposium 1995, Proceedings, 9th International, April 25-28, 1995, IEEE Comput. Soc., pages 466-471, XP010135921					1995
N.C./	I	Ma et al., "E-Kernel: An Embedding Kernel on the IBM Victor V256, Multiprocessor for Program Mapping and Network Reconfiguration, IEEE Transactions on Parallel and Distributed Systems, IEEE Service Center, vol. 5, no. 9, September 1, 1994, pages 977-994, XP000460490					September 1, 1994
EXAMINER Yi Chen/				DATE CONSIDERED 11/26/2007			
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### U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
Y.C./A		4,868,818	09/19/1989	Madan	371	11.3	10/29/1987
Y.C./B		5,020,059	05/28/1991	Gorin et al.	371	11.3	03/31/1989
Y.C./C		5,748,882	05/05/1998	Huang	395	184.01	05/08/1996
Y.C./D		5,926,619	07/20/1999	Badovinatz et al.	395	182.02	01/05/1998
Y.C./E		6,453,426	09/17/2002	Gamache et al.	714	4	03/26/1999
Y.C./F		6,629,266	09/30/2003	Harper et al.	714	38	11/17/1999
Y.C./G		2003/0005276	01/02/2003	French et al.	713	2	06/28/2001
Y.C./H		2003/0097487	05/22/2003	Rietze et al.	709	325	11/20/2001

### FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
Y.C./I		WO 03/005192	01/16/2003	PCT	G06F	9/445	X	
Y.C./J		EP 0 981 089 A2	02/23/2000	European Patent Office	G06F	11/14	X	

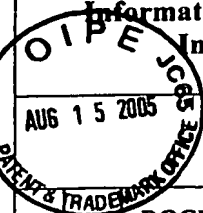
### NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
Y.C./K		Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012313, 7 pages, 09/20/2005, and Written Opinion of the International Search Authority, 7 pages	09/20/2005
Y.C./L		Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012316, 7 pages, 09/14/2005, and Written Opinion of the International Search Authority, 7 pages	09/14/2005
Y.C./M		Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012242, 7 pages, 09/19/2005, and Written Opinion of the International Search Authority, 9 pages	09/19/2005
Y.C./N		Davidson, U.S. Patent Application Serial Number 10/991,754, filed 11/17/2004, entitled "Fault Tolerance and Recovery in a High-Performance Computing (HPC) System, 117 pages	11/17/2004

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Y.C./E	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012031, 6 pages, 08/01/2005 and Written Opinion of the International Search Authority, 5 pages					08/01/2005
Y.C./F	Chang et al., "Performance Improvement of Allocation Schemes for Mesh-Connected Computers," Journal of Parallel and Distributed Computing, Academic Press, Duluth, MN, vol. 52, no. 1, July 10, 1998, 30 pages					07/10/1998
Y.C./G	Wanqian, et al. "Non-Contiguous Processor Allocation Algorithms for Distributed Memory Multicomputers," Supercomputing '94, Proceedings, Washington, D.C., November 14-18, 1994, 10 pages					11/14/1994 - 11/18/1994
Y.C./H	Krevat et al., "Job Scheduling for the BlueGene/L System," Lecture Notes in Computer Science, vol. 2537, July 24, 2002, 18 pages					07/24/2002
I						
J						
EXAMINER  Yi Chen/				DATE CONSIDERED  11/26/2007		
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NON-PATENT DOCUMENTS		
	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
Y.C./	E Communication from the European Patent Office, European Search Report for Application No. EP 05 25 2235, 3 pages, 07/22/2005, 3 pages	07/22/2005
Y.C./	F Rzymianowicz et al., "Clustering SMP Nodes with the ATOLL Network: A Look into the Future of System Area Networks," Proceedings of High Performance Computing, 8th International Conference, May 8, 2000, 10 pages	05/08/2000
Y.C./	G Bhanot et al., "The BlueGene/L Supercomputer," 20th International Symposium on Lattice Field Theory," vol. 119, June 2002, 8 pages	06/2002
Y.C./	H Advanced Micro Devices et al., "Hypertransport Technology I/O Link - A High-Bandwidth I/O Architecture," July 20, 2001, pp. 1-25	07/20/2001
Y.C./	I Pinkston et al., "InfiniBand: The "De Facto" Future Standard for System and Local Area Networks or Just a Scalable Replacement for PCI Buses?," Cluster Computing-Kluwer Academic Publishers, vol. 6, no. 2, 2003	2003
Y.C./	J Culler et al., "Parallel Computer Architecture - A Hardware/Software Approach, Interconnection Network Design," Morgan Kaufmann, 1999, 30 pages	1999

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--------------------------	-----------------------------------

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		Docket Number 064747.1016		Group Art Unit	Filing Date April 15, 2004

U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

FOREIGN PATENT DOCUMENTS								
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	J							
	K							
	L							
	M							
	N							

NON-PATENT DOCUMENTS		
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)
Y.C./	O	Patent Application entitled, "High Performance Computing System and Method", 47 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1012
Y.C./	P	Patent Application entitled, "System and Method for Cluster Management Based on HPC Architecture", 49 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1014
Y.C./	Q	Patent Application entitled, "System and Method for Detecting and Managing HPC Node Failure", 49 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1015
Y.C./	R	Patent Application entitled, "System and Method for Topology-aware Job Scheduling and Backfilling in an HPC Environment", 49 pages specification, claims and abstract, 10 pages of drawings, inventors Ballew et al, Attorney Docket No. 064747.1017
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EXAMINER  <div style="text-align: right; font-family: cursive;">/Yi Chen/</div>	DATE CONSIDERED  <div style="text-align: center; font-family: cursive;">11/26/2007</div>
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.